

## Solid State Broadband High Power Amplifier

**2135 – BBS3G6QHM**
**300 – 3800 MHz / 50 Watts**

The BBS3G6QHM (2135) is suitable for ultra broadband high power linear applications, laboratory, and RFI/EMC susceptibility testing. This dual band amplifier utilizes MOSFET/LDMOS Amp for low-band and GaAsFET Amps for high-band frequency. Employing advanced broadband RF matching networks and combining techniques, EMI/RFI filters, and all qualified components achieve exceptional performance, and high efficiency. The system includes a universal voltage, single phase, power supply and a built-in forced air-cooling system. Empower RF's ISO9001 Quality Assurance Program assures consistent performance and the highest reliability.



SKU#: 2135DLFAAXLXX

- Solid-state class AB and class A design
- Instantaneous ultra broadband
- Small form factor and lightweight
- Suitable for CW, AM and FM (Consult factory for other modulation types)
- 50 ohm input/output impedance
- High reliability and ruggedness
- Built-in control, monitoring and protection circuits

### ELECTRICAL SPECIFICATIONS @ 120V<sub>AC</sub>, 25°C, 50 Ω System

Parameter	Symbol	Min	Typ	Max	Unit
Operating Frequency ( Dual Band)	BW - Low	300		1000	MHz
	BW - High	800		3800	
Output Power CW	P <sub>SAT</sub> - Low	72	80		Watt
	P <sub>SAT</sub> - High	45	50		
Output Power @ 1dB Gain Compression	P <sub>1dB</sub> - Low	50			Watt
	P <sub>1dB</sub> - High		50		
Power Gain @ 1dB Gain Compression	G <sub>1dB</sub>	46	48		dB
Input Power for Rated P <sub>SAT</sub>	P <sub>IN</sub>		0	3	dBm
Gain Flatness	ΔG			±2.0	dB
Gain Adjustment Range	FGA	25			dB
Input Return loss	S <sub>11</sub>			-10	dB
Noise Figure	NF		10		dB
Third Order Intercept Point 2-Tone @ 42dBm/Tone, 100kHz Spacing	IP3		+56		dBm
Harmonics @ P <sub>OUT</sub> = 50W	2 <sup>ND</sup> - Low		-40		dBc
	2 <sup>ND</sup> - High		-20		
	3 <sup>RD</sup>		-20		
Spurious Signals	Spur			-73	dBc
Operating Voltage (1-phase)	V <sub>AC</sub>	100		240	Volt
Power Consumption @ P <sub>OUT</sub> = 50W	P <sub>D</sub>		600		Watt

### MECHANICAL SPECIFICATIONS

Parameter	Value	Units
Dimensions	19 x 5.25 x 22	Inch
Weight	57	lb.
RF Connectors Input/Output	Type-N, Female	
Cooling	Internal forced-air cooling system	

### ENVIRONMENTAL CHARACTERISTICS (Design to Meet)

Parameter	Symbol	Min	Typ	Max	Unit
Operating Ambient Temperature	T <sub>A</sub>	0		+50	°C
Non-operating Temperature	T <sub>STG</sub>	-40		+85	°C
Relative Humidity (non-condensing)	RH			95	%
Altitude (MIL-STD-810F Method 500.4)	ALT			30,000	Feet
Vibration/Shock MIL-STD-810F – Method 514.5/516.5 – Proc I	VI/SH		Airborne		

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**LIMITS**

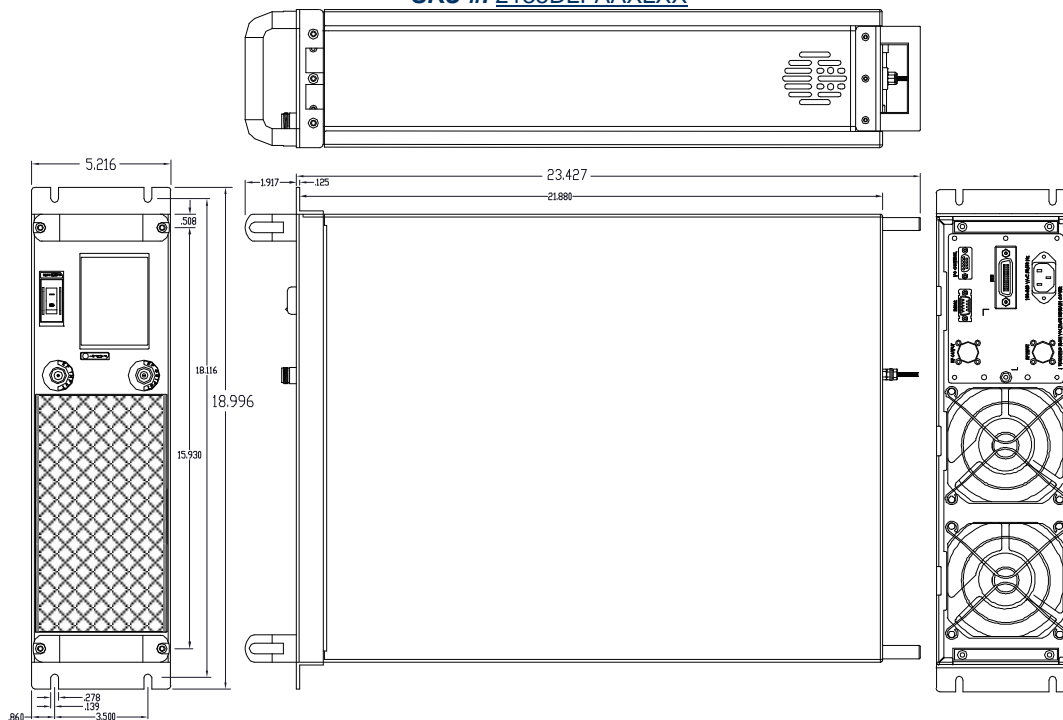
Input RF drive level without damage	+6 dBm	Max
Load VSWR @ P <sub>OUT</sub> = 50W	∞ @ all load phase & amplitude for duration of 1 minute 3:1 @ all load phase & amplitude continuous	-
Thermal Overload	85°C shutdown	Max

**AVAILABLE OPTIONS**

SKU #	Description	LCD Touchscreen
2135DEFAAXLXX	LCD controller, Ethernet, Front RF connectors 100-240VAC, 50/60Hz	Touchscreen Digital Display, including FWD/REV Power indication (dBm or Watt scale), Gain Adjustment, ALC Fast/Slow, On/Off, Standby mode, Fault indication, Rear panel GPIB/HPIB IEEE-488.2 and Half Duplex RS232.
2135DERAAXLXX	LCD controller, Ethernet, Rear RF connectors 100-240VAC, 50/60Hz	
2135DLFAAXLXX	LCD controller, Front RF connectors 100-240VAC, 50/60Hz	
Optional	Rack Slides (Call for price)	

**I/O INTERFACE CONNECTOR – D-Sub 9-Pin, Female**

Pin #	Description	Specifications
1	FWD Test Point	Analog Voltage 0-5V <sub>DC</sub> relative to Forward Power Level
2	REV Test Point	Analog Voltage 0-5V <sub>DC</sub> relative to Reverse Power Level
3	5V Test Point	+5.0V <sub>DC</sub> ±0.2V
4	VVA Test Point	+5.6V <sub>DC</sub> ±0.2V
5	EXT Shutdown	Disable: TTL Logic High (5V) (Internally Pulled-Low)
6	12V Test Point	+12.0V <sub>DC</sub> ±0.5V
7	P/S1 Test Point	+12.0-15.0V <sub>DC</sub>
8	P/S2 Test Point	+26.0-30.0V <sub>DC</sub>
9	GND	Ground

**SYSTEM OUTLINE SHOWN**
**SKU #: [2135DLFAAXLXX](#)**


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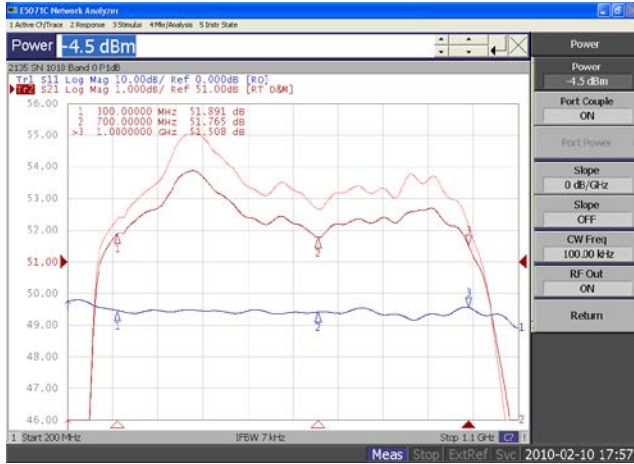
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## TYPICAL PERFORMANCE PLOTS – LOW BAND

### Plot 1 – Small Signal Gain and $P_{1dB}$

Top Curve: Small Signal Gain @  $P_{IN} = -20dBm$   
 Middle Curve: Power Gain @  $P_{1dB}$ ,  $P_{IN} = -4.5dBm$   
 Reference: 51dB, 1dB/Div.  
 Bottom Curve: Input Return Loss  
 Reference: 0dB, 10dB/Div.



### Plot 2 – Small Signal Gain and $P_{SAT}$

Top Curve: Small Signal Gain @  $P_{IN} = -20dBm$   
 Middle Curve: Power Gain @  $P_{SAT}$ ,  $P_{IN} = -0.5dBm$   
 Reference: 50dB, 1dB/Div.  
 Bottom Curve: Input Return Loss  
 Reference: 0dB, 10dB/Div.



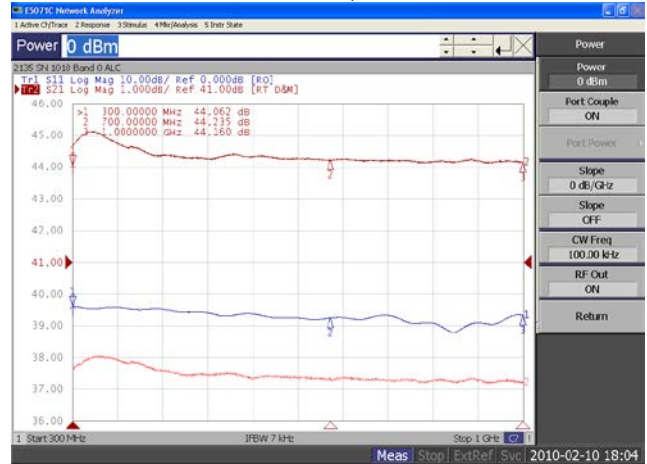
### Plot 3 – Gain Adjustment Range

Top Curve: Maximum Gain @  $P_{IN} = -20dBm$   
 Middle Curve: Minimum Gain @  $P_{IN} = -20dBm$   
 Reference: 20dB, 10dB/Div.  
 Bottom Curve: Input Return Loss @ Minimum Gain  
 Reference: 0dB, 10dB/Div.



### Plot 4 – ALC Flatness

Top Curve: ALC @ 25W,  $P_{IN} = 0dBm$   
 Bottom Curve: ALC @ 5W,  $P_{IN} = 0dBm$   
 Middle Curve: Input Return Loss  
 Reference: 0dB, 10dB/Div.



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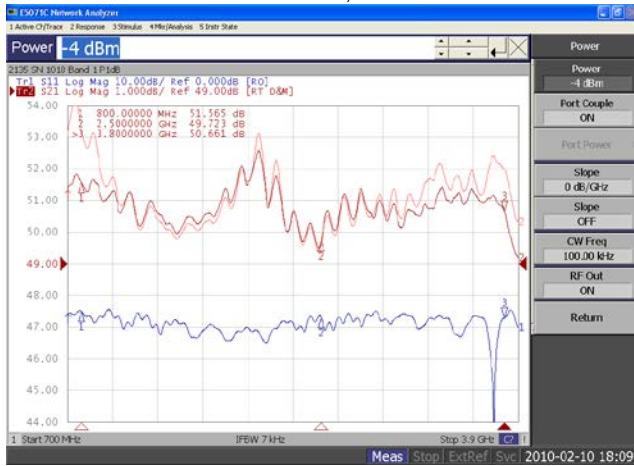
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## TYPICAL PERFORMANCE PLOTS – HIGH BAND

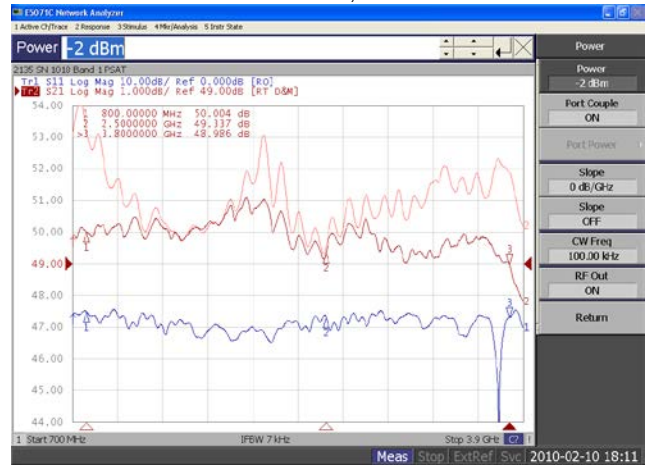
### Plot 5 – Small Signal Gain and $P_{1dB}$

Top Curve: Small Signal Gain @  $P_{IN} = -20dBm$   
 Middle Curve: Power Gain @  $P_{1dB}$ ,  $P_{IN} = -4dBm$   
 Reference: 49dB, 1dB/Div.  
 Bottom Curve: Input Return Loss  
 Reference: 0dB, 10dB/Div.



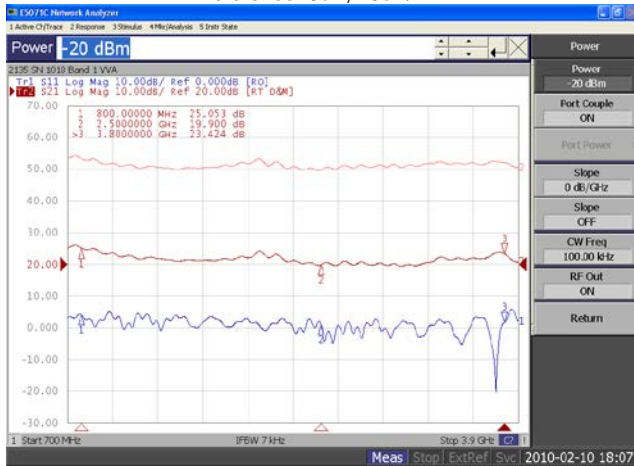
### Plot 6 – Small Signal Gain and $P_{SAT}$

Top Curve: Small Signal Gain @  $P_{IN} = -20dBm$   
 Middle Curve: Power Gain @  $P_{SAT}$ ,  $P_{IN} = -2dBm$   
 Reference: 49dB, 1dB/Div.  
 Bottom Curve: Input Return Loss  
 Reference: 0dB, 10dB/Div.



### Plot 7 – Gain Adjustment Range

Top Curve: Maximum Gain @  $P_{IN} = -20dBm$   
 Middle Curve: Minimum Gain @  $P_{IN} = -20dBm$   
 Reference: 20dB, 10dB/Div.  
 Bottom Curve: Input Return Loss @ Minimum Gain  
 Reference: 0dB, 10dB/Div.



### Plot 8 – ALC Flatness

Top Curve: ALC @ 25W,  $P_{IN} = 0dBm$   
 Bottom Curve: ALC @ 5W,  $P_{IN} = 0dBm$   
 Middle Curve: Input Return Loss  
 Reference: 41dB, 1dB/Div.  
 Reference: 0dB, 10dB/Div.

