

# MODULES

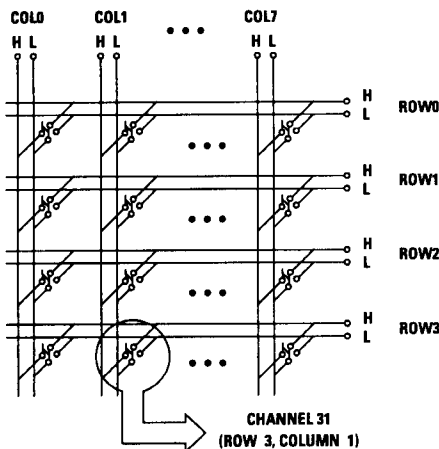
## 4 x 8 Matrix Module

### Agilent N2262A

- Multiple inputs connecting to multiple outputs
- High-speed switching in parallel operation
- Built-in relay cycle counters

The N2262A 4x8 matrix module contains 32 cross points organized in a 4-row by 8-column configuration. It provides a convenient way to connect a group of test instruments to multiple test points on DUTs. Each cross point in the module switches two wires for the high and low measurement. Multiple matrix modules can be connected for applications that require large matrices. For example, four N2262As can be combined as a 16x8

matrix. N2262As can be used in conjunction with other modules (such as multiplexer modules) to provide a wide variety of switching combinations. More than one switch can be closed at the same time, allowing any combination of rows to be connected to columns. Up to eight channels can be operated in parallel for high-speed switching. Three module accessories are available to simplify wiring.



### Specifications

#### General Specifications

Relays:	Armature latching relay
Thermal Offset:	<3 $\mu$ V
Relay Life	
Mechanical:	10 <sup>8</sup>
Electrical:	5x10 <sup>5</sup> ( at 1A load)

#### Input Characteristics

Maximum Voltage:	200V
Maximum Current:	
Per channel	1A
Per module	4A
Maximum Power:	
Per channel	60W or 62.5VA
Per module	240W or 250 VA
Initial Closed	
Channel Resistance:	<1 $\Omega$

#### DC Isolation

Open Channel, Channel-Channel:	<(40°C, 50% RH) >10 <sup>10</sup> $\Omega$
HI-LO:	<(40°C, 50% RH) >10 <sup>10</sup> $\Omega$
Channel-Chassis:	<(40°C, 50% RH) >10 <sup>10</sup> $\Omega$

#### AC Isolation

Capacitance (with 1 channel closed):	
Open Channel, Channel-Channel	<7pF
HI-LO	<30pF
Channel-Chassis	<50pF
Insertion Loss (with 50 $\Omega$ termination):	
100kHz	<0.10dB
1MHz	<0.20dB
10MHz	<0.60dB
Crosstalk (with 50 $\Omega$ termination):	
100kHz	<-73dB
1MHz	<-53dB
10MHz:	<-28dB

#### Module Accessories

N2292A	Screw terminal block
N2296A	Crimp & insert terminal block
N2298A	DIN96-to-D25